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APPLICANTS Nobuyuki Sekikawa, Ashikagai-shi, JAPAN; Masaaki Momen, Ojiya-shi, JAPAN; Wataru Andoh, Kamo-shi, JAPAN; Koichi Hirata, Sanjyo-shi, JAPAN; <i>Yes, sn</i>				
** CONTINUING DATA ***** This application is a DIV of 09/925,628 08/10/2001 PAT 6,690,070				
** FOREIGN APPLICATIONS ***** JAPAN 2000-242617 08/10/2000 <i>Yes, sn</i>				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 03/11/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after met Verified and <i>AL</i> Allowance <i>SL (7/6/06)</i> Acknowledged Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 3	TOTAL CLAIMS 6
				INDEPENDENT CLAIMS 2
ADDRESS Barry E. Bretschneider Morrison & Foerster LLP Suite 300 20001650 Tysons Boulevard McLean, VA22102				
TITLE Method of manufacturing insulated gate semiconductor device				
FILING FEE RECEIVED 770	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	